IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In re PATENT APPLICATION of:

Applicant(s) : Jun KANAMORI)

Appln. No. : To Be Assigned

Filed : August 6, 2003) INFORMATION) DISCLOSURE) STATEMENT

For : SEMICONDUCTOR DEVICE FABRICATION METHOD USING

FABRICATION METHOD USING OXYGEN ION IMPLANTATION

Atty. Dkt. : MAE 292)_____

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of two Japanese publications. Any relevance of the Japanese publications can be gleaned from the attached English-language Abstracts, and from page 1 of the present application where these documents are discussed. The documents are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted,

August 6, 2003

Date

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket MAE 292		Application No. To Be Assigned						
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